

**IN THE CLAIMS:**

Please amend the claims as follows. (All claims listed)

1. (Currently Amended) A processing agent to transfer data of a predetermined data line length in an external transaction, the agent comprising:

an internal cache having a plurality of cache lines, each cache line sized to store a memory address and multiple data line lengths of data; and

a transaction queue having a plurality of [[a]] queue entries, the queue entries each further comprising:

a primary sub entry including an address portion and status portion, the status portion provided for a first external transaction corresponding to a first data portion of a respective cache line, and

at least a secondary sub entry including a status portion provided for a second external transaction corresponding to a second data portion of the respective cache line, wherein the first and second data portions each stores a quantity of data corresponding to a data line.

2. (Previously Presented) The processing agent of claim 1, wherein the cache lines each include a tag portion to store the memory address information.

3. (Previously Presented) The processing agent of claim 2, wherein the internal cache further comprises

match detection logic for the tag portion, and

control logic provided in communication with the match detection logic.

4. (Previously Presented) The processing agent of claim 1, wherein the cache lines each include a cache coherency state field in association with each data line length of data.

5. (Canceled).

6. (Previously Presented) The agent of claim 1, wherein the status portion of the primary sub entry includes a field representing whether the first external transaction is part of a multiple transaction sequence.

7. (Previously Presented) The agent of claim 1, wherein a total number of primary and secondary entries of the respective cache line equals a multiple number of data line lengths provided in the respective cache line.

8-10. (Canceled).

11. (Previously Presented) A processing agent, comprising:  
  
an internal cache having cache lines each sized to store a memory address and multiple data lines; and  
  
a transaction queue system comprising a plurality of queue entries, each queue entry to provide a sequence of external transactions, each external transaction of the sequence related to a single data line, the sequence of external transactions related to a single cache line,  
  
wherein the internal cache and the transaction queue system each receive data requests on a common input.

12. (Previously Presented) The processing agent of claim 11, wherein the internal cache and the transaction queue system communicate by signal lines.

13. (Previously Presented) The processing agent of claim 12, wherein the signal lines include a cache hit signal line and a tag hit signal line.

14. (Previously Presented) The processing agent of claim 11, wherein each queue entry comprising:

a primary entry including an address portion and status portion, the status portion provided for a first external transaction of the agent, and

a secondary entry including a status portion provided for a second external transaction.

15. (Previously Presented) The transaction queue of claim 14, wherein the status portion of the primary entry includes a field representing whether the first transaction is part of a multiple transaction sequence.

16. (Previously Presented) The transaction queue of claim 14, further comprising control logic adapted to cycle through the queue entries and post transactions therefrom.

17-33. (Canceled).

34. (Previously Presented) The agent of claim 6, wherein when the first external transaction is part of a multiple transaction sequence, the second external transaction is a next transaction of the multiple transaction sequence.

35. (Previously Presented) The transaction queue of claim 34, wherein the second external transaction addresses a second address adjacent to a first address stored in the address portion.

36. (Previously Presented) The transaction queue of claim 35, wherein the second address is the first address incremented by one.

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37. (Canceled)